

Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. M122-1987		SERIAL NO. Filed Herewith	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Robert Kerr et al.			
				FILING DATE Filed Herewith		GROUP Unknown	
U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
CL	AA	5,206,187	04/93	Doan et al.	437	192	
	AB	5,731,610	03/98	Rhodes	257	309	
	AC	5,162,890	11/92	Butler	257	306	
		5,173,752	12/92	Motonami et al.	257	309	
	AE	5,272,367	12/93	Dennison et al.	257	306	
	AF	5,317,193	05/94	Watanabe	257	774	
	AG	5,444,003	08/95	Wang et al.	437	31	
	AH	5,510,648	04/96	Davies et al.	257	657	
	AI	5,547,892	08/96	Wuu et al.	437	52	
	AJ	5,552,620	09/96	Lu et al.	437	195	
CL	AK	5,612,240	03/97	Chang	437	44	
	AL						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes No
	AM						
	AN						
	AO						
	AP						
	AQ						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
CL	AR		Gray & Meyer, "Analysis and Design of Analog Integrated Circuits", Third Edition, John Wiley & Sons, Inc., 1993, pg. 169				
	AS						
	AT						
EXAMINER				DATE CONSIDERED			
calvin				5.4.04			
<small>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.</small>							